

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (currently amended) A method of buffering data within a first-in first-out buffer comprising:
 - a. receiving a stream of data to be buffered within the first-in first-out buffer;
 - b. storing the stream of data within the first-in first-out buffer thereby forming a stored stream of data;
 - c. obtaining a series of program instructions at the first-in first-out buffer specifying operations to be performed on the stored stream of data; and
 - d. generating an output stream of data by executing the series of program instructions and performing the operations to manipulate the stored stream of data while within the first-in first-out buffer ~~on the stored stream of data.~~
2. (original) The method as claimed in claim 1 wherein the program instructions are obtained from an application.
3. (original) The method as claimed in claim 1 wherein the program instructions are obtained from a control circuit.
4. (original) The method as claimed in claim 1 wherein the stream of data is received from a bus structure.
5. (original) The method as claimed in claim 4 wherein the bus structure is an IEEE 1394 bus structure.
6. (original) The method as claimed in claim 1 further comprising transmitting the output stream of data from a buffer interface.
7. (original) The method as claimed in claim 1 wherein generating an output stream includes synchronizing the output stream of data to a time reference.

8. (original) The method as claimed in claim 7 wherein the time reference is a cycle time of an IEEE 1394 bus structure.
9. (currently amended) A method of buffering data within a first-in first-out buffer comprising:
- a. receiving a stream of data to be buffered within the first-in first-out buffer;
 - b. storing the stream of data within the first-in first-out buffer thereby forming a stored stream of data;
 - c. obtaining a series of program instructions at the first-in first-out buffer specifying operations to be performed in relation to the stored stream of data; and
 - d. generating an output stream of data by executing the series of program instructions and performing the operations to manipulate the stored stream of data while within the first-in first-out buffer ~~in relation to the stored stream of data~~, including synchronizing the output stream of data to a time reference.
10. (original) The method as claimed in claim 9 wherein the program instructions are obtained from an application.
11. (original) The method as claimed in claim 9 wherein the program instructions are obtained from a control circuit.
12. (original) The method as claimed in claim 9 wherein the stream of data is received from a bus structure.
13. (original) The method as claimed in claim 12 wherein the bus structure is an IEEE 1394 bus structure.
14. (original) The method as claimed in claim 9 further comprising transmitting the output stream of data from a buffer interface.
15. (original) The method as claimed in claim 9 wherein generating an output stream of data includes manipulating the stored stream of data to form the output stream of data.

16. (original) The method as claimed in claim 9 wherein the time reference is a cycle time of an IEEE 1394 bus structure.
17. (currently amended) An apparatus for buffering data within a first-in first-out buffer comprising:
- a. means for receiving a stream of data to be buffered within the first-in first-out buffer;
 - b. means for storing the stream of data within the first-in first-out buffer thereby forming a stored stream of data;
 - c. means for obtaining a series of program instructions specifying operations to be performed on the stored stream of data; and
 - d. means for generating an output stream of data by executing the series of program instructions and performing the operations to manipulate the stored stream of data while within the first-in first-out buffer on the stored stream of data;
- ~~wherein the means for obtaining and the means for generating are within the first-in first-out buffer.~~
18. (original) The apparatus as claimed in claim 17 wherein the program instructions are obtained from an application.
19. (original) The apparatus as claimed in claim 17 wherein the program instructions are obtained from a control circuit.
20. (original) The apparatus as claimed in claim 17 wherein the means for receiving a stream of data is coupled to a bus structure to receive the stream of data.
21. (original) The apparatus as claimed in claim 20 wherein the bus structure is an IEEE 1394 bus structure.
22. (original) The apparatus as claimed in claim 17 further comprising means for transmitting coupled to the means for generating for transmitting the output stream of data.
23. (original) The apparatus as claimed in claim 17 wherein the means for generating synchronizes the output stream of data to a time reference.

24. (original) The apparatus as claimed in claim 23 wherein the time reference is a cycle time of an IEEE 1394 bus structure.
25. (currently amended) A programmable first-in first-out buffer comprising:
- a. an input interface circuit configured to receive a stream of data to be buffered within the first-in first-out buffer;
 - b. a data memory coupled to the input interface circuit to store the stream of data, thereby forming a stored stream of data;
 - c. a program memory configured to obtain and store a series of program instructions specifying operations to be performed on the stored stream of data; and
 - d. an execution unit coupled to the program memory and to the data memory to generate an output stream of data by executing the series of program instructions and perform the operations on the stored stream of data while the stored stream of data is within the data memory of the first-in first-out buffer.
26. (original) The programmable first-in first-out buffer as claimed in claim 25 wherein the program instructions are obtained from an application.
27. (original) The programmable first-in first-out buffer as claimed in claim 25 wherein the program instructions are obtained from a control circuit.
28. (original) The programmable first-in first-out buffer as claimed in claim 25 wherein the input interface circuit is coupled to a bus structure to receive the stream of data.
29. (original) The programmable first-in first-out buffer as claimed in claim 28 wherein the bus structure is an IEEE 1394 bus structure.
30. (original) The programmable first-in first-out buffer as claimed in claim 25 further comprising an output interface circuit coupled to the execution circuit and the data memory and configured to transmit the output stream of data.
31. (original) The programmable first-in first-out buffer as claimed in claim 25 wherein the execution unit synchronizes the output stream of data to a time reference.

32. (original) The programmable first-in first-out buffer as claimed in claim 31 wherein the time reference is a cycle time of an IEEE 1394 bus structure.
33. (currently amended) A system comprising:
- a. a bus interface circuit configured to couple to a bus structure and receive a stream of data;
 - b. a data memory coupled to the bus interface circuit to store the stream of data, thereby forming a stored stream of data, wherein the data memory stores and outputs the stored stream of data, thereby forming an output stream of data;
 - c. a program memory configured to obtain and store a series of program instructions specifying operations to be performed on the stored stream of data; and
 - d. an execution unit coupled to the program memory and to the data memory to generate the output stream of data by executing the series of program instructions and performing the operations on the stored stream of data while the stored stream of data is within the data memory.
34. (original) The system as claimed in claim 33 wherein the program instructions are obtained from an application.
35. (original) The system as claimed in claim 33 wherein the program instructions are obtained from a control circuit.
36. (original) The system as claimed in claim 33 wherein the bus structure is an IEEE 1394 bus structure.
37. (original) The system as claimed in claim 33 further comprising an output interface circuit coupled to the execution unit and the data memory and configured to transmit the output stream of data.
38. (original) The system as claimed in claim 33 wherein the execution unit synchronizes the output stream of data to a time reference.
39. (original) The system as claimed in claim 38 wherein the time reference is a cycle time of an IEEE 1394 bus structure.

40. (currently amended) A network of devices comprising:
- a. a plurality of devices;
 - b. a bus structure coupled between the plurality of devices to transmit data between the devices; and
 - c. a programmable first-in first-out buffer including:
 - i. an input interface circuit configured to receive a stream of data to be buffered within the first-in first-out buffer;
 - ii. a data memory coupled to the input interface circuit to store the stream of data, thereby forming a stored stream of data;
 - iii. a program memory configured to obtain and store a series of program instructions specifying operations to be performed on the stored stream of data; and
 - iv. an execution unit coupled to the program memory and to the data memory to generate an output stream of data by executing the series of program instructions and perform the operations on the stored stream of data while the stored stream of data is within the data memory of the first-in first-out buffer.
41. (original) The network of devices as claimed in claim 40 wherein the program instructions are obtained from an application.
42. (original) The network of devices as claimed in claim 40 wherein the program instructions are obtained from a control circuit.
43. (original) The network of devices as claimed in claim 40 wherein the bus structure is an IEEE 1394 bus structure.
44. (original) The network of devices as claimed in claim 40 further comprising an output interface circuit coupled to the execution unit and the data memory and configured to transmit the output stream of data.
45. (original) The network of devices as claimed in claim 40 wherein the execution unit synchronizes the output stream of data to a time reference.

46. (original) The network of devices as claimed in claim 45 wherein the time reference is a cycle time of an IEEE 1394 bus structure.